Applicants : William R. Wheeler, et al.   Attorney's Docket No.: 10559-597001
Serial No. : 09/941,952   Intel Ref.: P12881
Filed   : August 29, 2001
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AMENDMENT TO THE SPECIFICATION:

Please replace the abstract on page 26 with the following new abstract:

Simulating a logic design having combinatorial logic and state logic includes representing the combinatorial logic and the state logic using separate graphic elements, identifying clock domains for the combinatorial logic and the state logic using the separate graphic elements, generating computer code that simulates operation of portions of the logic design, the computer code being generated based on the clock domains, and associating the computer code with graphic elements that correspond to the portions of the logic design.